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Sheet 1 of 1

Complete if Known

Application Number	08/902133
Filing Date	July 29, 1997
First Named Inventor	Forbes, Leonard
Group Art Unit	2815
Examiner Name	Eckert II, George

Attorney Docket No: 00303.356US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
				345	206	
JSC	US-3792465	02/12/1974	Collins, D. R., et al	340	324R	12/30/1971
JSC	US-5393999	02/28/1995	Malhi, S.	257	289	06/09/1994
JSC	US-5557122	09/17/1996	Shrivastava, R., et al	257	309	05/12/1995
JSC	US-5661312	08/26/1997	Weitzel, C. E., et al	257	77	03/30/1995
JSC	US-5734181	03/31/1998	Ohba, R., et al	257	77	05/20/2002
JSC	US-5798548	08/25/1998	Fujiwara,	257	319	05/18/1996
JSC	US-5808336	09/15/1998	Miyawaki, M.	257	315	05/05/1995
JSC	US-5926740	07/20/1999	Forbes, L., et al	438	763	10/27/1997
JSC	US-5989958	11/23/1999	Forbes, L.	438	257	08/20/1998
JSC	US-6130147	10/10/2000	Major, J. S., et al	438	604	03/18/1997
JSC	US-6297521	10/02/2001	Forbes, L., et al	257	76	08/14/1998
JSC	US-6307775	10/23/2001	Forbes, L., et al	365	185.01	08/27/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
JSC	JP-2203564	08/13/1990	Fujii, Y., et al	H01L	29/46	
JSC	JP-57-126175	08/05/1982	Hamakawa, Y., et al	H01L	31/04	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JSC		BURNS, S.G., et al., In: <u>Principles of Electronic Circuits</u> , West Publishing Co., St. Paul, MN, (1987), 382-383	
JSC		BURNS, S., et al., In: <u>Principles of Electronic Circuits</u> , West Publishing Company, (1987), pp. 380	
JSC		CAPASSO, F., et al., "New Floating-Gate AlGaAs/GaAs Memory Devices with Graded-Gad Electron Injector and Long Retention Times", <u>IEEE Electron Device Letters</u> , (1988), pp. 377-379	
JSC		NG, K., In: <u>Complete Guide To Semiconductor Devices</u> , McGraw-Hill, Inc. New York, (1995), pp. 322-328, 605-608	
JSC		SHARMA, B., et al., "Heterojunction Devices", In: <u>Semiconductor Heterojunctions</u> , Pergamon Press, New York, (1974), pp. 133-137	
JSC		STREETMAN, B., In: <u>Solid State Electronic Devices</u> , 4th Edition, Prentice Hall, New Jersey, (1995), pp. 217-219, 392-394	
JSC		SZE, S.M., In: <u>Physics of Semiconductor Devices</u> , 2nd Edition, John Wiley & Sons, New York, (1981), pp. 122-129, 700-703, 708-710, 763-765	
JSC		WOLF, S., "Semiconductor Memory Process Integration", <u>Silicon Processing for The VLSI Era, Volume 2: Process Integration</u> , (1990), pp. 623-628	

EXAMINER

JSC

DATE CONSIDERED

12/30/02

Substitute Disclosure Statement Form (PTO-1449)

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